SP Devices interleaving technology used in Texas Instruments' new 12-bit ADC evaluation module

Linköping, Sweden, November 9, 2009 – SP Devices today introduced its second evaluation module (EVM) with Texas Instruments (TI). This new EVM combines TI's fastest 12-bit analog-to-digital converters (ADCs) in an interleaved fashion with a Xilinx® Virtex®-5 FPGA to create the best-performing high-speed digitizer solution in the market. The FPGA comes pre-installed with SP Devices' proprietary time-interleaving technology to eliminate interleaving spurs, which enhances performance and facilitates rapid system-level evaluation for wireless communications, military, test and measurement applications. The EVM joins TI's portfolio of support tools for customers using high-speed data converters in wide-bandwidth applications.

See http://focus.ti.com/docs/toolsw/folders/print/ads54rf63-adx4.html

The ADS54RF63-ADX4 is a 12-bit, 2.2 GSPS digitizer solution evaluation module. The EVM incorporates four 12-bit, 550 MSPS ADS54RF63 ADCs operating in an interleaved mode to create a 2.2 GSPS digitizer. The outputs of the four ADCs feed a Xilinx Virtex 5 FPGA which is pre-configured with proprietary interleaving technology from SP Devices.

The SP Devices' interleaving technology continuously analyzes the ADCs' outputs to automatically remove the interleaving spurs arising from gain and mismatch errors, removing the need for costly monitoring components and an off-line recalibration routine. The captured signal is then sent to a PC for further analysis. Those wishing to implement a system utilizing the interleaving technology found on the ADS54RF63-ADX4 will need to contact SP Devices in order to obtain a production license.

"It remains critical for our customers that we address the increasing demand for higher sampling speeds and extended bandwidth," said Jonas Nilsson, CEO of SP Devices. "Combining SP Devices' innovative interleaving technology with TI's market-leading data converters allows us to extend the performance of high-speed ADCs. This high performance will extend to a broad range of new applications including Radio Base Stations/Multi-Carrier systems, Software-defined radio, and beyond."

More information on these solutions is available from SP Devices at www.spdevices.com

About SP Devices
SP Devices (Signal Processing Devices Sweden AB) provides digital signal processing IP for the enhancement of analog-to-digital conversion. The IP products are available for implementation in ASICs or deployed on FPGA platforms. SP Devices' portfolio of products enables customers to build systems with state-of-the-art analog-to-digital performance that enables advances in the area of software defined radio, radio base station transceivers, digital imaging, high-speed data acquisition and broadband communication. Additional company and product information is available at www.spdevices.com.

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